

### **AMENDMENTS TO THE CLAIMS**

The following listing of claims will replace all prior versions and listings of claims in the application.

1. **(Currently Amended)** A circuit adapted to connect a driver circuit and an optical assembly, said circuit comprising:

a first transmission line adapted to deliver a first signal from the driver circuit to the optical assembly, said first transmission line comprising a first end adapted to connect to the driver circuit and a second end adapted to connect to the optical assembly;

a second transmission line used to bias said first signal, said second transmission line being electrically connected to said second end of said first transmission line; and

a flexible member that includes the first and second transmission lines,

wherein said first transmission line comprises a matching impedance, and

wherein said second transmission line is electrically connected to said first transmission line at a connection point that is located between said matching impedance and said optical assembly such that no matching impedance is positioned between said optical assembly and said connection point.

2. **(Canceled)**

3. **(Canceled)**

4. **(Previously Presented)** A circuit as recited in claim 1, wherein said optical assembly comprises a laser diode.

5. **(Previously Presented)** A circuit as recited in claim 1, wherein an end of said second transmission line is electrically connected to a direct current source.

6. **(Previously Presented)** A circuit as recited in claim 1, wherein said first signal is an alternating current signal.

7. **(Currently Amended)** A transceiver for use in transceiving signals, the transceiver comprising:

a first transmission line comprising a first end, a first end matching impedance, a second end, and a second end matching impedance, said first transmission line electrically connected at said first end to a means for generating modulated signals and electrically connected at said second end to a means for generating optical signals based upon said modulated signals;

a flexible member including first and second insulating layers between which a portion of the first transmission line is positioned; and

means for biasing said modulated signals electrically connected to said second end of said first transmission line at a connection point that is located between said second end matching impedance and said means for generating optical signals; ~~means for biasing said modulated signals.~~

8. **(Previously Presented)** A transceiver as recited in claim 7, wherein said means for generating modulated signals comprises a laser driver.

9. **(Original)** A transceiver as recited in claim 7, wherein said means for generating optical signals comprises a laser diode.

10. **(Original)** A transceiver as recited in claim 7, further comprising a flexible circuit incorporating said first transmission line and said second transmission line and electrically connecting said means for generating modulated signals to said means for generating optical signals based upon said modulated signals.

11. **(Original)** A transceiver as recited in claim 7, further comprising a current source, said current source configured to deliver a bias current to said means for generating optical signals.

12. **(Original)** A flexible circuit as recited in claim 7, wherein an end of said second transmission line is electrically connected to a direct current source.

13. **(Previously Presented)** A transceiver, comprising:  
a driver circuit adapted to deliver a signal to an optical assembly along a first transmission line, said first transmission line comprising a first end electrically connected to said driver circuit and a second end electrically connected to said optical assembly;  
a current source in communication with said optical assembly and adapted to provide a bias current to said optical assembly;  
a second transmission line electrically connecting said current source to said optical assembly, said second transmission line being connected to said first transmission line;  
a voltage source configured such that a current output associated with the voltage source is responsive to regulation by the current source;  
a third transmission line comprising a first end electrically connected to said driver circuit and a second end electrically connected to said optical assembly; and  
a fourth transmission line electrically connecting said voltage source to said optical assembly, said fourth transmission line being electrically connected to said third transmission line.

14. **(Original)** A transceiver as recited in claim 13, wherein said driver circuit is a laser driver circuit.

15. **(Original)** A transceiver as recited in claim 13, wherein said signal is delivered to said optical assembly at a rate of at least 10 Gigabits/second.

16. **(Original)** A transceiver as recited in claim 13, wherein said signal is delivered to said optical assembly at a rate of less than 10 Gigabits/second.

17. **(Original)** A transceiver as recited in claim 13, wherein said voltage source is a direct current source.

18. **(Original)** A transceiver as recited in claim 13, wherein said first transmission line and said second transmission line are incorporated within a flexible circuit.

19. **(Original)** A transceiver as recited in claim 13, wherein said first transmission line further comprises at least one matching impedance.

20. **(Original)** A transceiver as recited in claim 19, wherein said second transmission line connects to said first transmission line between said at least one matching impedance and said optical assembly.

21. **(Original)** A transceiver as recited in claim 19, wherein said at least one matching impedance is between 5  $\Omega$  and 25  $\Omega$ .

22. **(Original)** A transceiver as recited in claim 19, wherein said current source generates a bias current, said bias current flowing to said optical assembly without passing through said at least one matching impedance.

23. **(Original)** A transceiver as recited in claim 13, wherein said optical assembly comprises a laser diode.

24. **(Original)** A transceiver as recited in claim 13, wherein said second transmission line has a load of between 5  $\Omega$  and 10 $\Omega$ .

25. **(Currently Amended)** A transceiver, comprising:
- a driver circuit adapted to generate a modulated driver signal deliverable to an optical assembly;
  - a current source in communication with said optical assembly and adapted to provide a bias current for said optical assembly; and
  - a circuit electrically connecting at least two of said driver circuit, said direct current source, and said optical assembly, said circuit comprises:
    - a first transmission line electrically connected to said driver circuit at a first end and to said optical assembly at a second end, said first transmission line being adapted to allow said modulated signal to be delivered to said optical assembly;
    - a second transmission line electrically connected to said current source and to said optical assembly, said second transmission line being connected to said second end of said first transmission line; and
    - first and second flexible insulating layers between which portions of the first and second transmission lines are disposed,
- wherein said first transmission line comprises at least one matching impedance,  
and  
wherein said second transmission line is connected to said first transmission line at a connection point that is located between said at least one matching impedance and said optical assembly such that no matching impedance is positioned between said optical assembly and said connection point.

26. **(Canceled)**

27. **(Canceled)**

28.     **(New)** A transceiver as recited in claim 7,  
          wherein said first end matching impedance is configured to match an impedance of the  
means for generating modulated signals,  
          wherein said second end matching impedance is configured to match an impedance of the  
means for generating optical signals, and  
          wherein the second end matching impedance has a greater impedance value than the first  
end matching impedance.